## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1217	passive NEAR cooling	US-PGPUB; USPAT	OR	ON	2006/10/16 15:45
L2	63	(passive NEAR cooling) AND throttling	US-PGPUB; USPAT	OR	ON	2006/10/16 15:46
L3	31	(passive NEAR cooling) AND throttling AND voltage AND frequency	US-PGPUB; USPAT	OR	ON	2006/10/16 15:47
L4	2	(passive NEAR cooling) AND throttling AND voltage AND frequency AND simulation	US-PGPUB; USPAT	OR	ON	2006/10/16 15:49
L5	21	(passive NEAR cooling) AND throttling AND voltage AND frequency AND (simulation OR simulate OR simulating OR model OR modeling)	US-PGPUB; USPAT	OR	ON	2006/10/16 15:50

## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	248749	(simulate OR simulation OR simulating OR model OR modeling) AND (thermal OR heat OR overheating OR cooling) AND (management OR regulation OR control OR regulate)AND (processor OR micro-processor OR computer chip OR chip OR CPU OR core OR central processing unit OR silicon chip OR pentium OR pentium "4" OR pentium IV OR microcontroller OR micro-controller)	US-PGPUB; USPAT	OR	ON	2006/10/16 10:48
L3	125700	(simulate OR simulation OR simulating OR model OR modeling) AND (thermal OR heat OR overheating OR cooling) AND (management OR regulation OR control OR regulate)AND (processor OR micro-processor OR computer chip OR chip OR CPU OR core OR central processing unit OR silicon chip OR pentium OR pentium "4" OR pentium IV OR microcontroller OR micro-controller) AND (voltage OR frequency)	US-PGPUB; USPAT	OR	ON .	2006/10/16 11:04
L4	39493	(simulate OR simulation OR simulating OR model OR modeling) AND (thermal OR heat OR overheating) AND (management OR regulation OR control OR regulate)AND (processor OR microprocessor OR microprocessor OR computer chip OR chip OR CPU OR core OR central processing unit OR silicon chip OR pentium OR pentium "4" OR pentium IV OR microcontroller OR micro-controller) AND (voltage AND frequency)	US-PGPUB; USPAT	OR	ON	2006/10/16 11:07

10/16/2006 2:05:12 PM C:\Documents and Settings\jplante\My Documents\EAST\Workspaces\Plante\_Test.wsp Page 1

## **EAST Search History**

L5	152	(simulate OR simulation OR simulating OR model OR modeling) AND (thermal OR heat OR overheating) AND (management OR regulation OR control OR regulate)AND (processor OR microprocessor OR microprocessor OR computer chip OR chip OR CPU OR core OR central processing unit OR silicon chip OR pentium OR pentium "4" OR pentium IV OR microcontroller OR micro-controller) AND (voltage AND frequency) AND "703".CLAS.	US-PGPUB; USPAT	OR	ON	2006/10/16 13:40
L6	140	(simulate OR simulation OR simulating OR model OR modeling) AND ((thermal OR heat OR overheating) WITH (thermal management OR regulation OR control OR regulate)) AND (processor OR micro-processor OR computer chip OR chip OR CPU OR core OR central processing unit OR silicon chip OR pentium OR pentium "4" OR pentium IV OR micro-controller OR micro-controller) AND (voltage AND frequency) AND "703".CLAS.	US-PGPUB; USPAT	OR	ON	2006/10/16 14:04

10/16/2006 2:05:12 PM
C:\Documents and Settings\jplante\My Documents\EAST\Workspaces\Plante\_Test.wsp